

Profile

1	NameoftheFaculty	Shweta Biradar		pht	
2	Dateofjoining	01/11/2022			
3	Email id	Shwetabiradar28@gmail.com			
4	Designation	ASST. Professor			
5	Department	Electronics and communications engineering			
6	EducationQualifications	M.Tech			
7	WorkExperience	Teaching	Research	Industry	Others
				2+ years	
8	AreaofSpecialization	VLSI Design and Embedded System			
9	Courses taught at Diploma/ PostDiploma/ Under Graduate/ PostGraduate/PostGraduateDiplo ma Level				
10	No.ofpaperspublishedinNational/InternationalJournals/Conferences				
	Journals	National	International		
			01		
	Conferences	National	International		
			01		
	ResearchGuidance				
	MasterDegree	Completed	Ongoing		
Ph.D.					

11	ProjectsCarriedout		
12	Patents		
13	TechnologyTransfer		
14	1)Anuradha savadi, Raju Yanashetti, Shweta Biradar, “ Design and Implementation of 64-bit IIR filters using Vedic Multipliers”,in ScienceDirect Procedia Computer Science 85(2016) 790-797, in International Conference on Computational Modeling and Security (CMS 2016), https://www.sciencedirect.com/science/article/pii/S1877050916306172 2) Shruti Biradar, Shweta Biradar, Somnath Biradar, “Design of a Low Power and High Performance Digital Multiplier Using a 6T Adder”, in International Journal of Ethics in Engineering & Management Education Website: www.ijeee.in (ISSN: 2348-4748, Volume 1, Issue 7, June 2014), https://ijeee.in/wp-content/uploads/2014/08/IJEEE-60-63.pdf		