

Prof. Dr. Sharnbasveshwar Agasthi  
President, Sharnbasveshwar Vidya Vardhak Sangha  
Kalaburagi, Karnataka, IndiaProf. Dr. Mahesh C. Dr. Subhasini S. Agasthi  
Vice President, Sharnbasveshwar Vidya Vardhak Sangha  
Kalaburagi, Karnataka, IndiaProf. Dr. Chaitanya Chaitanya Agasthi  
Member, Sharnbasveshwar Vidya Vardhak Sangha  
Kalaburagi, Karnataka, India

**ಶರಣಬಸವ**  
**Sharnbasva**  
Kalaburagi-585 103 Karnataka-India | Estd. : 2017



**ವಿಶ್ವವಿದ್ಯಾಲಯ**  
**University**  
Kalaburagi-585 103 - ಕರ್ನಾಟಕ - ಭಾರತ | ಸ್ಥಾಪನೆ : 2017

Prof. Dr. Mahesh Chaitanya Agasthi  
Member, Sharnbasveshwar Vidya Vardhak Sangha  
Kalaburagi, Karnataka, IndiaProf. Dr. Chaitanya Chaitanya Agasthi  
Member, Sharnbasveshwar Vidya Vardhak Sangha  
Kalaburagi, Karnataka, India

A Private University enacted by Govt. of Karnataka as "Sharnbasva University Act 2012"  
Karnataka Act No. 17 of 2013. Notification No. ED 144 URC 2016 dated 29/07/2017

**Faculty of Engineering & Technology (Co-Education) and**  
**Faculty of Engineering & Technology (Exclusively for Women)**

ECE Dept.

**2022-23**  
**(Even Sem)**

## Department of Electronics & Communication Engineering

### ACTIVITY REPORT

<b>Title of the Event</b>	<b>Two days Faculty Development Program on Full-Custom and Semi-Custom Design using Cadence Tool.</b>
<b>Date of Activity held</b>	20/04/2023 to 21/04/2023
<b>Time of Activity</b>	10:00 AM - 5:00 PM
<b>Type of Activity (Cultural/Cocurricular/Curricular)</b>	Co-Curricular Activity
<b>Chief Guest</b>	<b>Mr. Shivaprasad</b>
<b>Professional details of Chief Guest</b>	Senior Application Engineer, Entuple Technologies, Bangaluru.
<b>Program/Course/ Class</b>	Faculty Development Program
<b>Number of Staff attended</b>	25
<b>Activity Incharge</b>	Dr. Anuradha Savadi, Dr. Rekha, Prof. Shruti V H, Prof. Vijaylaxmi M K
<b>Description of Activity</b>	Mr. Shivaprasad, Senior Application Engineer Entuple Technologies, Bangaluru, conducted two days Faculty Development Program on Full- Custom and Semi-Custom Design using Cadence Tool. On 20 <sup>th</sup> April 2023 he described Introduction to Full Custom IC Design Flow, Cadence Solutions for Custom IC Design, Schematic Capture using Virtuoso, Schematic Editor Symbol Creation, Testbench Creation using Virtuoso Schematic Editor, Functional Simulation using Spectre, Layout Design using Virtuoso Layout Editor, Physical Verification which includes DRC & LVS, Parasitic Extraction using Quantus, Post Layout Simulation Generation of GDSII. On 21 <sup>st</sup> conducted session on Introduction to Semi-Custom IC Design Flow, > Cadence Solutions for Semi-Custom IC Design, Functional Verification using Incisive, RTL Synthesis using Genus Synthesis Solution, Physical Implementation using Innovus that includes, Floor Planning (✓ Power Planning ✓ Placement ✓ CTS ✓ Routing ) Timing Analysis, Power Analysis, Parasitic Extraction.

#### Activity Photographs





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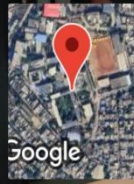
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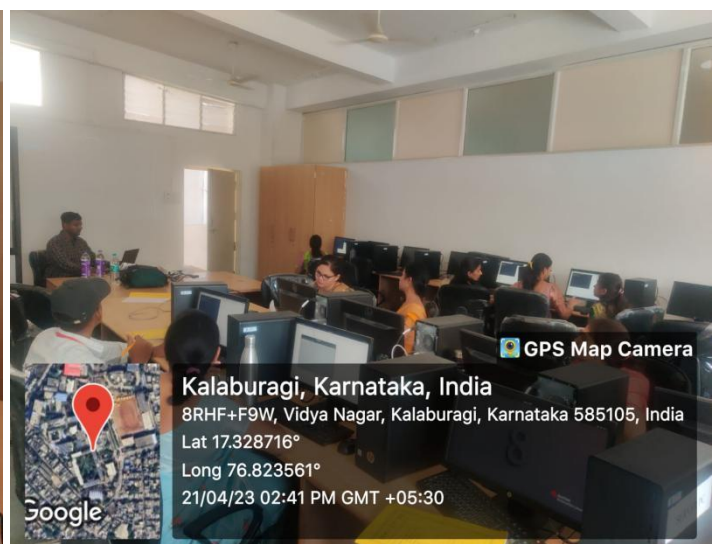
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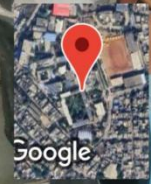
GPS Map Camera



Kalaburagi, Karnataka, India  
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Lat 17.328733°  
Long 76.82359°  
21/04/23 05:00 PM GMT +05:30



GPS Map Camera



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