Date: 12-6-2022

FACULTY OF ENGINEERING & TECHNOLOGY (CO-EDUCATION)
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
REPORT ON
INVITED TALK BY MS JYOTI BIRADAR, DFT ENGINEER,ANALOG DEVICES, BENGALURU,
KARNATAKA

Topic Covered: **Career Projections after Engineering VLSI PERSPECTIVE**

1. TITLE: Career Projections After Engineering **DATES: 12-6-2022**
2. Preamble: Invited talk on "Career Projections After Engineering" was conducted on 12-6-2022 by the Department of Electronics & Communication Engineering. The talk is to make the UG and PG students to understand the Career opportunities RPA field after the completion of their graduation.
3. Participant's information: The students of 6th semester, M.Tech in Digital Electronics-1st semester and M.Tech. in VLSI & ES 1st semester have attended the session. Total 94 students attended the session
4. Description about the invited talk: In the inaugural function Dr. Shivkumar Jawaligi, Dean, Faculty of Engg. & Tech. inaugurated the session. Dr. Savita Patil, Prof. Dept. of ECE (Co-Education) introduced the speaker to the gathering. The speaker covered the following contents in his talk: ➢ Preparation by an engineering student during graduation. ➢ VLSI courses online and offline. ➢ Skills needed by an VLSI Engineer ➢ Future of VLSI front end and backend Technologies ➢ Industries of VLSI ➢ What VLSI market expect from an Engineer ➢ Preparedness for an future roles VLSI perspective The session was very informative and students have shown good response. At the end of the session Dr. Savita Patil Prof. Dept. of ECE (Co-Education) proposed vote of thanks to the gathering.

GPS Map Camera

Kalaburagi, Karnataka, India

8RHF+CHV, Vidya Nagar, Kalaburagi, Karnataka 585105, India
Lat 17.329142°
Long 76.823928°
12/06/22 12:40 PM

Figure1- Felicitation to the Guest Speaker by Dr. Shivkumar Jawaligi, Dean Sharnbasva University, Kalaburagi

Figure-2-Participants attending the session

Course outcome: This talk enables the students to apply the Very Large Scale Integration (VLSI) technology in the semiconductor industry.

COURSE OUTCOME AND PROGRAMME OUTCOME MAPPING (1/2/3):
Note: 1-Low,   2-Medium, 3-High

CO/PO	PO.1	PO.2	PO.3	PO.4	PO.5	PO.6	PO.7	PO.8	PO.9	PO.10	PO.11	PO.12	PSO.1	PSO.2
CO	-	-	-	--	3	-	-	-	-	-	-	2	3	3

Impact of the invited talk: This talk motivates the students to acquire the knowledge of VLSI technology and choose their carrier in Semiconductor industry.