Date: 20/4/2025

FACULTY OF ENGINEERING & TECHNOLOGY (CO-EDUCATION)
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

REPORT ON

Industry INVITED TALK BY Mr. Bhimashankar Verification Engineer, Cadence Design Systems Bengaluru.

1. TITLE: Scope of VLSI in Semiconductor Industries **DATES: 20-4-2025.**
2. Preamble: Industry Invited talk on "Scope of VLSI in Semiconductor Industries" is organized by the Department of Electronics & Communication Engineering. The talk is to make the UG students to understand the VLSI design flow and verification, opportunities in semiconductor industries and Roles of VLSI Engineer.
3. Participant's information: The students of UG 6th Semester and total Participants are 93.

4. Description about the invited talk: In the inaugural function Dr. Shashidhar Sonnad, Chairman, Dept. of ECE (Co-Education) introduced the speaker to the gathering. The guest speaker is felicitated by Dr. Shivkumar Jawaligi, Dean, Faculty of Engg. & Tech. (Co-Eucation). Highlights of the talk:
➢ **VLSI-choosing the subdomain**
    • Introduction
    • Flow of Design and Verification
    • What are basic knowledge required for VLSI Engineer
    • Roles of VLSI Engineer
    • Why VLSI
➢ **Work opportunities in Semiconductor Industry**

Figure1- Felicitation to the Guest Speaker by Dr. Shashidhar Sonnad, Chairman Dept. of ECE, Sharnbasva University, Kalaburagi

Figure-2-Session conduction by Guest Speaker Mr. Bhimashankar

Figure-3- Participants attending the session

Course outcome: This talk enables the students to understand the semiconductor industry requirements and various job opportunities in the semiconductor industry.

COURSE OUTCOME AND PROGRAMME OUTCOME MAPPING (1/2/3):

Note: 1-Low,   2-Medium, 3-High

CO/PO	PO.1	PO.2	PO.3	PO.4	PO.5	PO.6	PO.7	PO.8	PO.9	PO.10	PO.11	PSO.1	PSO.2	PSO.3
CO	-	-	-	--	2	-	-	-	3	-	3	-	-	-

Impact of the invited talk: This talk will motivate the young minds to take up their carrier in Semiconductor industry.