



Pooja Matoshree Dr. Dakshini S. Appa
Chairperson, Sharnbasveshwar Vidya Vardhak Sangha
Karnataka, Sharnbasveshwar, Mandya



Pooja Chiranjeevi Doddappa Appa
9th Bhavi Mahadesha Peethachhipati,
Sharnbasveshwar, Samathan



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10th Parvathi Peethachhipati, Sharnbasveshwar, Samathan
President, Sharnbasveshwar Vidya Vardhak Sangha
Karnataka, Sharnbasveshwar, Mandya

ಶರಣಬಸವ
Sharnbasva

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



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University

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ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
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Faculty of Engineering & Technology (Co-Education)

ECE Dept.

**2024-25 (Even
Sem)**

Department of Electronics & Communication Engineering ACTIVITY REPORT

Title of the Event	Two day workshop for students on “ Glimpses of VLSI: Concepta to Practice”
Date of Activity held	20/04/2025 to 21/04/2025
Time of Activity	10:00 AM - 5:00 PM
Type of Activity (Cultural/ Cocurricular/Curricular)	Co-Curricular Activity
Chief Guest	Dr. Mahesh Koti
Professional details of Chief Guest	Associate Professor, PES Engineering College, Mandya
Program/Course/ Class	Students workshop
Number of Staff attended	74
Activity Incharge	Dr. Revansiddappa Kinagi, Dr. Rekha S, Dr. Mahesh R K
Description of Activity	Dr. Mahesh Koti, Associate Professor, Dept. of ECE, PES Engineering College, Mandya conducted two day workshop on Glimpses of VLSI: Concepts to Practice. On 20 th April 2025 he started the session by describing, 1) Introduced the students to the domain of VLSI with its historical perspective. 2) Gave glimpses of semiconductor industry and its ecosystem, starting from EDA Vendors to Fab. 3) Brief overview of the ASIC flow with emphasis on the role of standard cells and their library. On 21 st April 2025, he covered 1) Introduced the students to OpenLane tool suite for ASIC flow. 2) Demo with practice installation of Ubuntu and OpenLane on Windows 11 Systems. 3) Synthesized netlist and GDSII file demonstration in Yosys and Magic. 4) Practical session in CADENCE tool suite. i) Writing TCL script file for genus. ii) Identifying and associating library for the design with emphasis of lef and lib files. Iii) Comparison of netlist with the design Verilog code. iv) Innovus Layout generation

Activity Photographs:





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20/04/25 10:25 AM GMT +05:30



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India
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20/04/25 10:24 AM GMT +05:30

Centenary Celebrated Sharnbasveshwar Vidya Vardhak Sangha's



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Channarayana University, Mysuru

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