



Pooja Matoshree Dr. Dakshayani S. Appa
Chairperson, Sharnbasveshwar Vidya Vardhak Sangha
Hosur 55C, Sharnbasveshwar Semsthan



Pooja Chiranjeevi Doddappa Appa
9th Bharvi Mahadascha Peethachipatti,
Sharnbasveshwar Semsthan



Pooja Dr. Sharnbasveshwar Appa
8th Pankajach Peethachipatti, Sharnbasveshwar Semsthan
President, Sharnbasveshwar Vidya Vardhak Sangha
Channarayana University, Kalyani

ಶರಣಬಸವ
Sharnbasva

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University

ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
7th Mahadascha Peethachipatti,
Sharnbasveshwar Semsthan
Founder President, Sharnbasveshwar
Vidya Vardhak Sangha



Matoshree Godutai
Doddappa Appa

www.sharnbasvauniversity.edu.in

Email : sharnbasvauniversity@gmail.com

Faculty of Engineering & Technology (Co-Education)
Department of Electronics and Communication Engineering

ECE Dept.

2024-25 (Even Sem)

Department of Electronics & Communication Engineering

ACTIVITY REPORT

Title of the Event	Two day workshop for students on “ VLSI FRONT-END DESIGN”
Date of Activity held	08/06/2025 to 09/6/2025
Time of Activity	10:00 AM - 5:00 PM
Type of Activity (Cultural/Cocurricular/Curricular)	Co-Curricular Activity
Chief Guest	Mr. Sharan Koti
Professional details of Chief Guest	FPGA Lead Engineer at Valeo, Hyderabad
Program/Course/ Class	Students workshop
Number of Staff attended	58
Activity Incharge	Dr. Rekha S and Dr. Mahesh R K
Description of Activity	<p>On Day-1-8-6-2025 the Session started with Introduction to VLSI & Industry What is VLSI?, Application in chips, mobile, automotive, , VLSI job roles: Front-end, Back-end, Verification, DFT Skills required for front-end VLSI engineer, Front-End VLSI Design Flow, HDL Design (Verilog/VHDL basics), Functional simulation, Synthesis, Static Timing Analysis (STA, RTL vs Gate-level vs Netlist, ASIC vs FPGA design flow, Activity: Quick walkthrough of a Verilog “4-bit counter” RTL example. In the afternoon session, Verilog Coding & Simulation, Verilog structure (module, inputs, always block, testbench), Writing a simple Verilog modul, Writing a testbench, Functional Simulation using ModelSim or open-source tool like Icarus Verilog . Activity: Hands-on demo (live or pre-recorded) – simulate a Verilog module. Synthesis & Timing Basics, What is logic synthesis, How RTL is mapped to gates,</p> <p>On Day-2-In the morning session covered the Introduction to synthesis tools (Yosys/OpenSTA/Synopsys Design Compiler), Constraints & timing reports, Show synthesis result and gate-level netlist. FPGA Introduction & Demo , FPGA basics (LUTs, Flip-Flops, Block RAM, IO), Introduction to Xilinx/Intel FPGAs, Overview of Vivado design flow, Bitstream generation & flashing, Simulated FPGA design or tool GUI walkthrough.</p> <p>The afternoon session was on- Career Guidance , How to prepare for VLSI career, Online resources, internships, certifications, Resume & interview tips. Successfully completed RTL design and verification of FIFO, 32-bit ALU, and UART Transmitter & Receiver using Verilog, Implemented testbenches for functional verification on Cadence’s NCLaunch. At last speaker conducted the mock interview session and given tips to how to face the interview.</p>



Pooja Matoshree Dr. Dakshini S. Appa
Chairman, Sharnbasveshwar Vidya Vardhak Sangha
Hosur 56, Sharnbasveshwar, Kalaburagi



Pooja Chiranjeevi Doddappa Appa
9th Bhavi Mahadasha Peethachipatti,
Sharnbasveshwar, Semthan



Pooja Dr. Sharnbasappa Appa
8th Mahadasha Peethachipatti, Sharnbasveshwar
President, Sharnbasveshwar Vidya Vardhak Sangha
Channarayana, Sharnbasveshwar, Kalaburagi

ಶರಣಬಸವ
Sharnbasva

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University

ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
7th Mahadasha Peethachipatti,
Sharnbasveshwar, Semthan
Founder President, Sharnbasveshwar
Vidya Vardhak Sangha



Matoshree Godutai
Doddappa Appa

www.sharnbasvauniversity.edu.in

Email : sharnbasvauniversity@gmail.com

Faculty of Engineering & Technology (Co-Education)
Department of Electronics and Communication Engineering

ECE Dept.

2024-25 (Even Sem)





Pooja Matoshree Dr. Dakshayani S. Appa
Chairman, Sharnbasveshwar Vidya Vardhak Sangha
Hosur 56, Sharnbasveshwar, Kalaburagi



Pooja Chiranjeevi Doddappa Appa
9th Bhavi Mahadesha Peethachipatti,
Sharnbasveshwar Semthan



Pooja Dr. Sharnbasappa Appa
8th Mahadesha Peethachipatti, Sharnbasveshwar Semthan
President, Sharnbasveshwar Vidya Vardhak Sangha
Channarayana, Sharnbasveshwar, Kalaburagi

ಶರಣಬಸವ
Sharnbasva

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University

ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
7th Mahadesha Peethachipatti,
Sharnbasveshwar Semthan
Founder President, Sharnbasveshwar
Vidya Vardhak Sangha



Matoshree Godutai
Doddappa Appa

www.sharnbasvauniversity.edu.in

Email : sharnbasvauniversity@gmail.com

Faculty of Engineering & Technology (Co-Education)
Department of Electronics and Communication Engineering

ECE Dept.

**2024-25 (Even
Sem)**





Pooja Matoshree Dr. Dakshayani S. Appa
Chairman, Sharnbasveshwar Vidya Vardhak Sangha
Hosur, Karnataka, India



Pooja Chiranjeevi Doddappa Appa
9th Bhavi Mahadasha Peethachipatti,
Sharnbasveshwar Samithan



Pooja Dr. Sharnbasappa Appa
8th Mahadasha Peethachipatti, Sharnbasveshwar Samithan
President, Sharnbasveshwar Vidya Vardhak Sangha
Channarayana, Karnataka, India

ಶರಣಬಸವ
Sharnbasva

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University

ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
7th Mahadasha Peethachipatti,
Sharnbasveshwar Samithan
Founder President, Sharnbasveshwar
Vidya Vardhak Sangha



Matoshree Godutai
Doddappa Appa

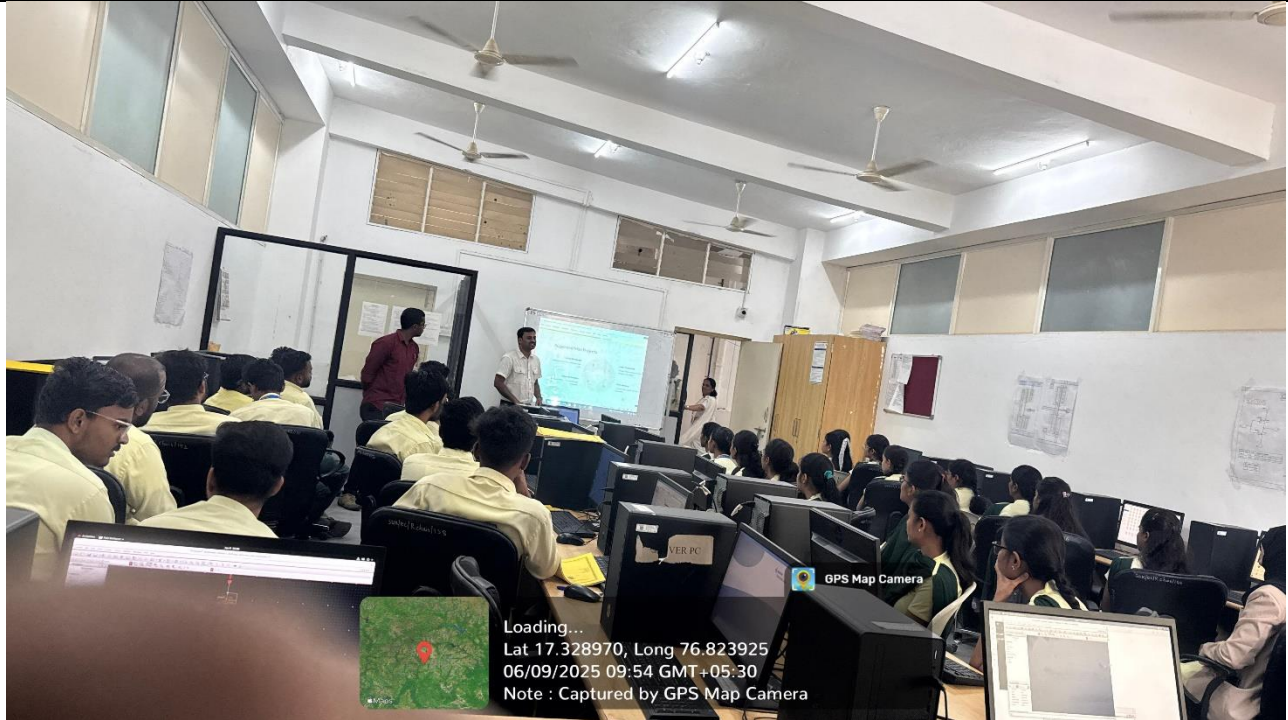
www.sharnbasvauniversity.edu.in

Email : sharnbasvauniversity@gmail.com

Faculty of Engineering & Technology (Co-Education)
Department of Electronics and Communication Engineering

ECE Dept.

2024-25 (Even Sem)





Pooja Matoshree Dr. Dakshini S. Appa
Chairperson, Sharnbasveshwar Vidya Vardhak Sangha
Hosur 55C, Sharnbasveshwar, Kalaburagi



Pooja Chiranjeevi Doddappa Appa
9th Bhavi Mahadasha Peethachipatti,
Sharnbasveshwar Samithan



Pooja Dr. Sharnbasveshwar Appa
8th Palakasha Peethachipatti, Sharnbasveshwar Samithan
President, Sharnbasveshwar Vidya Vardhak Sangha
Channarayana, Sharnbasveshwar, Kalaburagi

ಶರಣಬಸವ
Sharnbasva
Kalaburagi-585 103
Karnataka-India
Estd.: 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University
ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



Pooja Doddappa Appa
7th Mahadasha Peethachipatti,
Sharnbasveshwar Samithan
Founder President, Sharnbasveshwar
Vidya Vardhak Sangha



Matoshree Godutai
Doddappa Appa

www.sharnbasvauniversity.edu.in

Email : sharnbasvauniversity@gmail.com

Faculty of Engineering & Technology (Co-Education)
Department of Electronics and Communication Engineering

ECE Dept.

2024-25 (Even Sem)



Kalaburagi, Karnataka, India
8rhf+chv, Vidya Nagar, Kalaburagi, Karnataka 585105,
India
Lat 17.32894° Long 76.823857°
09/06/2025 03:12 PM GMT +05:30



Kalaburagi, Karnataka, India
8rhf+chv, Vidya Nagar, Kalaburagi, Karnataka 585105,
India
Lat 17.328949° Long 76.823876°
09/06/2025 02:44 PM GMT +05:30

Day-2-Session-4- Photographs