



ಶರಣಬಸವ
Sharnbasva
ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University
Kalaburagi-585 103
Karnataka-India
Estd.: 2017



FACULTY PROFILE

PERSONAL DETAILS	
Name	Dr. Nagaveni K
Designation	Associate Professor and Chairperson
Department	Electronics & Communication Engineering
Qualification	B.E (ECE), M.Tech.(Embedded Systems), Ph.D
Date of Joining	01-08-2017
Nature of Association	Regular
Contact No.	9900956886
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ACADEMIC DETAILS				
Sl.No.	Name of the Degree	University/Board	Part Time/Full Time	Year of Graduation/Award
01	Ph.D	VTU, Belagavi	Part Time	2024
02	M.Tech.	JNTU, Hyderabad	Full Time	2013
03	B.E	VTU, Belagavi	Full Time	2004

PROFESSIONAL DETAILS					
Sl.No.	Post Held	Organization	Period		Total Experience
01	Associate Professor	FETW, Sharnbasva University, Klb	2024	Till Date	15.5 yrs
02	Assistant Professor	FETW, Sharnbasva University, Klb	2017	2024	
03	Assistant Professor	GECW, Kalaburagi	2013	2017	
04	Lecturer	KBNCE,Kalaburagi	2010	2011	
Area of Interest					
Embedded Systems, Digital Image Processing, Internet of Things, Universal Human values					

Citation Metrics		
h-Index	i 10Index	Total Citations
2	0	14

RESEARCH PUBLICATIONS					
National Journals	International Journals	National Conferences	International Conferences	Scopus/Web of Science Publications	Total
	6		4		10

BOOK PUBLISHED		
SL. No.	Title of the Book	ISBN number
01	Digital Electronics	978-33-5515-145-2

RESEARCH WORK FOR PATENTS			
SL. No.	Patent No.	Title	Status with Date
1		Natural Language Processing Based Patient Medical Conditions Assessment System	Published
2		A Novel Machine Learning And Deep Learning Approach To Predict Liver Cirrhosis	Published 14/02/2024
3	499700-001	AI Based Early Health Risk Prediction Device	Published

PROFESSIONAL BODY MEMBERSHIP	
Sl. No.	Name of the Professional Body
01	IETE
02	Institute of Engineers
03	IAENG

RESEARCH SUPERVISION DETAILS				
Sl. No.	Name of the Scholar	Title	Year of Registration	Status

FDP/SEMINAR/WORKSHOP/CONFERENCE ORGANIZED		
Sl. No.	FDP/SEMINAR/WORKSHOP/CONFERENCE	Number
01	FDP's organised	04
02	Seminar's	10

AREA OF INTEREST
Digital Image Processing, Internet of Things, Image Processing, Signal Processing, Deep Learning, Machine Learning.

SUBJECTS TAUGHT
UG • Hardware Description Language. • Embedded Systems. • Basic Electronics • Logic Design • Research Methodology • Universal Human Values • Cryptography and network Security. • Soft Skills • Reliability Engineering PG • Advanced Embedded System • Real Time Operating Systems

Sl. No.	ADDITIONAL INFORMATION
01	Google Scholar_id: https://scholar.google.com/citations?user=hCyG1M0AAAAJ&hl=en
02	ORCID: 0009-0002-7119-8029

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