



Prof. Dr. Dababji S. Appa
President, Sharnbasveshwar Vidya Vardhaka Sangha
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Prof. Chiranjeevi Doddappa Appa
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Prof. Dr. Sharnbasappa Appa
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Haveli, Chandola, Sharnbasveshwar University, Kalaburagi

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Sharnbasva

ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



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University

Kalaburagi-585 103
Karnataka-India
Estd.: 2017



Prof. Doddappa Appa
V. P. Narasimhaiah Prasthishthan,
Sharnbasveshwar Sansthan
Founder President, Sharnbasveshwar
Vidya Vardhaka Sangha



Matoshree Godutal
Doddappa Appa

FACULTY PROFILE

PERSONAL DETAILS	
Name	Dr. Sujata K
Designation	Associate Professor
Department	Electronics & Communication Engineering
Qualification	B.E (ECE), M.Tech.(Communication Systems), Ph.D, PDF
Date of Joining	01-08-2019
Nature of Association	Regular
Contact No.	7338446968
Email id	sujatak@suk.edu.in

ACADEMIC DETAILS				
Sl.No.	Name of the Degree	University/Board	Part Time/Full Time	Year of Graduation/Award
01	PDF	Srinivasan University, Mangaluru	Part Time	2022
02	Ph.D	VTU, Belagavi	Part Time	2020
03	M.Tech.	VTU, Belagavi	Full Time	2010
04	B.E	VTU, Belagavi	Full Time	2008

PROFESSIONAL DETAILS					
Sl.No.	Post Held	Organization	Period		Total Experience
01	Associate Professor	Sharnbasva University, Klb	2019	Till date	16 years
03	Associate Professor	GECW, Kalaburagi	2013	2019	
04	Assistant Professor	GECW, Kalaburagi	2012	2013	
05	Assistant Professor	APPA IET, Kalaburagi	2010	2012	
Area of Interest					
Digital Electronics, Communication, Signal Processing, Image Processing, VLSI					

Citation Metrics		
h-Index	i10 Index	Total Citations
5	1	53

RESEARCH PUBLICATIONS					
National Journals	International Journals	National Conferences	International Conferences	Scopus/Web of Science Publications	Total

00	20	03	03	06	26
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BOOK PUBLISHED		
SL. No.	Title of the Book	ISBN number

RESEARCH WORK FOR PATENTS			
SL. No.	Patent No.	Title	Status with Date
01	201931033309 A	System of Fuzzy Controller for Real-Time Adjustment of Visual Sensor	Published-13-09-2019
02	202041007574 A	Method of Face Recognition using Combined Hidden Markov Model (SMM) and Singular Value Decomposition (SVD)	Published-28/02/2020

PROFESSIONAL BODY MEMBERSHIP	
Sl. No.	Name of the Professional Body
01	IEEE Senior Member
02	ISTE Life time Membership
03	IETE Life time Membership
04	Member of IAENG
05	Vidwaan

RESEARCH SUPERVISION DETAILS				
Sl. No.	Name of the Scholar	Title	Year of Registration	Status

FDP/SEMINAR/WORKSHOP/CONFERENCE ORGANIZED		
Sl. No.	FDP/SEMINAR/WORKSHOP/CONFERENCE	Number
01	Faculty Development Program	01
02	National Conference	01
03	National Level Seminar	01

AREA OF INTEREST
Digital Electronics, Communication, Image Processing,VLSI

SUBJECTS THOUGHT

- Digital Signal Processing
- Digital Signal System
- Network Analysis
- Signals & Systems
- Microcontroller -8051
- Microprocessor -8085
- Microprocessor -8086
- Power Electronics
- Basic Electronics
- Electronics Circuits & Logic Design
- Introduction to Digital Communication
- VLSI circuits
- Advanced Digital Communication.
- Wireless & Mobile Networks
- Advanced Communication Networks
- Advanced Digital Signal Processing
- Error Control Coding
- LTE 4G & 5G for Wireless Communication

Sl. No.	ADDITIONAL INFORMATION
01	Google Scholar: https://scholar.google.com/citations?user=-mFstnYAAAAJ&hl=en
02	Orchid : 0000-0001-5526-8141

