



ಶರಣಬಸವ
Sharnbasva
ಕಲಬುರಗಿ-585 103
ಕರ್ನಾಟಕ-ಭಾರತ
ಸ್ಥಾಪನೆ : 2017



ವಿಶ್ವವಿದ್ಯಾಲಯ
University
Kalaburagi-585 103
Karnataka-India
Estd.: 2017



FACULTY PROFILE

PERSONAL DETAILS	
Name	Shweta Biradar
Designation	Assistant Professor
Department	Electronics & Communication Engineering
Qualification	B.E (ECE), M.Tech.(VLSI Design and Embedded Systems)
Date of Joining	01-11-2022
Nature of Association	Regular
Contact No.	9886728263
Email id	shwetabiradar@suk.edu.in

ACADEMIC DETAILS				
Sl.No.	Name of the Degree	University/Board	Part Time/Full Time	Year of Graduation/Award
01	M.Tech.	VTU, Belagavi	Full Time	2015
02	B.E	VTU, Belagavi	Full Time	2013

PROFESSIONAL DETAILS					
Sl.No.	Post Held	Organization	Period		Total Experience
01	Assistant Professor	Sharnbasva University, Klb	2022	Till Date	6 years 4 months
02	FPGA/RTL Design Engineer	L & T Technology Services, Bengaluru	2018	2019	
03	Electronics Engineer	ADA, Bengaluru	2016	2018	
Area of Interest					
VLSI Design, Digital Design, Robotics and Artificial Intelligence.					

RESEARCH PUBLICATIONS					
National Journals	International Journals	National Conferences	International Conferences	Scopus/Web of Science Publications	Total
00	03	00	01	00	04

PROFESSIONAL BODY MEMBERSHIP	
Sl. No.	Name of the Professional Body
01	Life Member of- The Indian Society for Technical Education (ISTE)
02	Life Member of- The Institution of Electronics and Telecommunication Engineers (IETE)

FDP/SEMINAR/WORKSHOP/CONFERENCE ORGANIZED		
Sl. No.	FDP/SEMINAR/WORKSHOP/CONFERENCE	Number
01	Faculty Development Program	01
02	Workshop	02

AREA OF INTEREST
VLSI Design, Digital Design, Robotics and Artificial Intelligence.

SUBJECTS TAUGHT
● Introduction to IoT Technology ● VLSI Circuits ● Digital System Design ● Fundamentals of C Programming

Sl. No.	ADDITIONAL INFORMATION
01	VIDWAN ID - 667196