Profile

1	**Name of the Faculty**	Shobhana	
2	**Date of joining**	01/08/2014	
3	**Email id**	khanapurshobhana@gmail.com	
4	**Designation**	**Assistant Professor**	
5	**Department**	**E&CE**	
6	**Education Qualifications**	**M.Tech (PhD)**	

7	**WorkExperience**	**Teaching**	**Research**	**Industry**	**Others**
		8.5 Years			

8	**Area of Specialization**	**VLSI Design & Embedded System**

9	**Courses taught at Diploma/ Post Diploma/ Under Graduate/ Post Graduate/Post Graduate Diploma Level**	1. **Analog Circuits** 2. **Control System** 3. **Microcontrollers** 4. **Verilog HDL** 5. **Computer Organization** 6. **Basic Electronics** 7. **Analog Electronic Circuits** 8. **VLSI Circuits** 9. **DSPA** 10. **Electronic Instrumentation**

	No. of papers published in National/International Journals/Conferences		
10	**Journals**	**National**	**International**
		-	1
	Conferences	**National**	**International**
		–	–
	Research Guidance		
	Master Degree	**Completed**	**Ongoing**

	Ph.D.		

11	**Projects Carried out**	**1**	**Design & simulation of 5G multiband antenna with phase shift operation**
12	**Patents**	**1**	**Machine learning based recognition of patients behavior using hand to mouth gestures to assess their physical & emotional states in possible prediction AI-datasets**
13	**Technology Transfer**		
14	1. A high throughput sort free VLSI architecture for wireless applications International Journal of Engineering & Technology (IJERT) Vol. 2 Issue 12, Dec-2013.		