

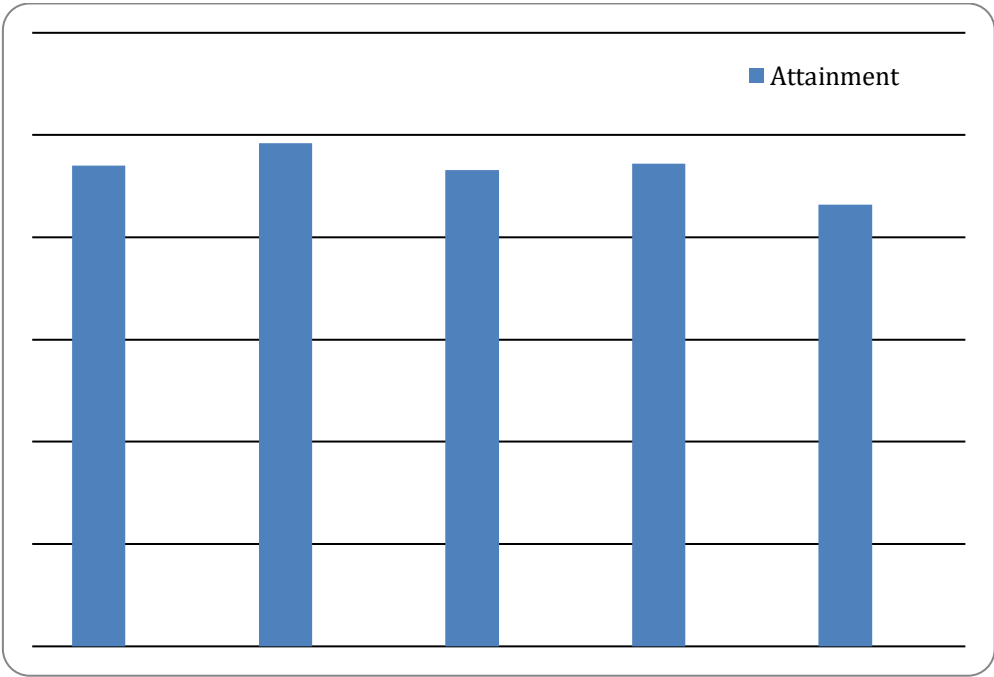
Faculty of Engineering and Technology (Exclusively for Women)
Department of Electronics & Communication Engineering

Title of the Event	Applications of Microwave waveguides and Antenna
Date of Activity held	04/11/2025
Time of Activity	2PM to 4PM
Type of Activity (Cultural/ Cocurricular/Curricula)	Cocurricular Activity
Resource Person	Mr. Subramanian R
Professional details of Resource Person	Application Manager Technilab Instrument Bangalore
Program/Course/ Class	B.Tech./ECE/5 th Semester
Number of Students attended	60
Number of Staff attended	10
Activity Incharge	Dr. Vanita K, Mrs. Vishalakshi Patil
Objective of the activity	To understand the basics of Microwave Waveguide and Antenna and its application
Description of Activity	This technical seminar on microwave waveguides and their applications would cover the fundamental principles, design, and various uses of waveguides in microwave engineering and highlighting their importance in modern communication systems.
Course Outcome	To understand the fundamental concepts of microwave waveguides and antenna systems and analyze the challenges and design considerations involved in implementing microwave and antenna systems in industry.
Outcome of the Activity	Students will be able to understand the fundamental principles of microwave propagation in waveguides and microwave antennas. Analyze different types of microwave waveguides and antennas (horn, reflector, microstrip, array used in practical systems. Students will be able to analyse the applications of microwave waveguides and antennas in communication.

CO's No.	CO Defined	Relevance to PO's and PSO'S
CO	To understand the fundamental concepts of microwave waveguides and antenna systems and analyze the challenges and design considerations involved in implementing microwave and antenna systems in industry.	PO1,PO2,PO3,PO4,PO5,PO10,PO12

Title of the Event: Technical Talk on “Applications of Microwave waveguides and Antenna”
Number of Students: 60

Q.No	Strongly Agree (3)		Moderately Agree (2)		Agree(1)		Total Score	%Attainment = (Total score/Total no. Students attended)	Average percentage Attainment	Attainment levels	Mapped POs	Mapped PSOs
1.	42	128	05	10	3	3	141	2.35	77.77	2	1,2,3,4,5, 10,12	1,2
2.	38	114	12	24	10	10	148	2.46				
3.	32	96	16	32	12	12	140	2.33				
4.	36	108	10	20	14	14	142	2.36				
5.	35	105	13	26	12	12	130	2.16				
Average								2.32				



Impact Analysis : From the above table , Technical Talk on “Emerging Trends and Opportunities in VLSI Domain” moderately attained to level 2. Against mapped PO1,PO2,PO3,PO5,P012 .

Note: High=3, Moderate=2 and Low=1

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Centenary Celebrated Sharnbasveshwar Vidya Vardhak Sangha's

Estd. : 2017



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Coordinator

Chairperson

Dean